

PATENT ABSTRACTS OF JAPAN

(11)Publication number : 2003-131620

(43)Date of publication of application : 09.05.2003

(51)Int.Cl. G09G 3/30
G09G 3/20

(21)Application number : 2001-331524

(71)Applicant : ASAHI KASEI MICROSYSTEMS KK

(22)Date of filing : 29.10.2001

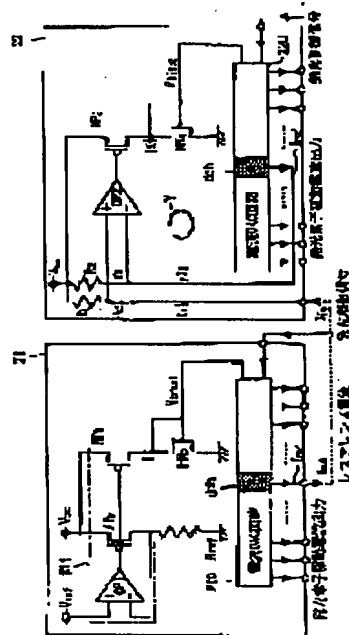
(72)Inventor : NAITO TAKASHI
IKEDA MASAKI

(54) DISPLAY PANEL DRIVING CIRCUIT

(57)Abstract:

PROBLEM TO BE SOLVED: To improve the picture quality of a display panel by making light emission intensity between adjacent IC chips uniform while reproducing the level of the light emission driving current of a master chip with the output level of the light emission driving current of a slave chip at high accuracy.

SOLUTION: In a first anode line driving circuit 21 being a master chip and a second anode line driving circuit 22 being a slave chip, driving outputs of respective dummy channels dchs are compared with each other in an operational amplifier OP2. Then, a bias voltage corresponding to the difference of both outputs is applied to a current DAC (digital-to-analog converter) circuit 220. As a result, since a feedback circuit like the allow Y is formed in the driving circuit, the level of the light emission driving current of the master chip can be reproduced at high accuracy with the output level of the light emission driving circuit of the slave chip and the light emission intensity between the adjacent IC chips is made uniform and then the picture quality of the display panel can be improved.



LEGAL STATUS

[Date of request for examination] 13.10.2004

[Date of sending the examiner's decision of rejection]

[Kind of final disposal of application other than the examiner's decision of rejection or application converted registration]

[Date of final disposal for application]

[Patent number] 3904888

[Date of registration] 19.01.2007

[Number of appeal against examiner's decision of rejection]

Searching PAJ

2/2 ページ

[Date of requesting appeal against examiner's
decision of rejection]

[Date of extinction of right]